

FIG. 1 CIRCUIT DIAGRAM PROVIDING AN EXAMPLE OF A PARALLEL-CONNECTED CIRCUIT FOR TWO SINGLE-PHASE PULSE-CONTROLLED INVERTERS WITH IGBTs, FREE-WHEELING DIODES, AND OUTPUT CHOKES

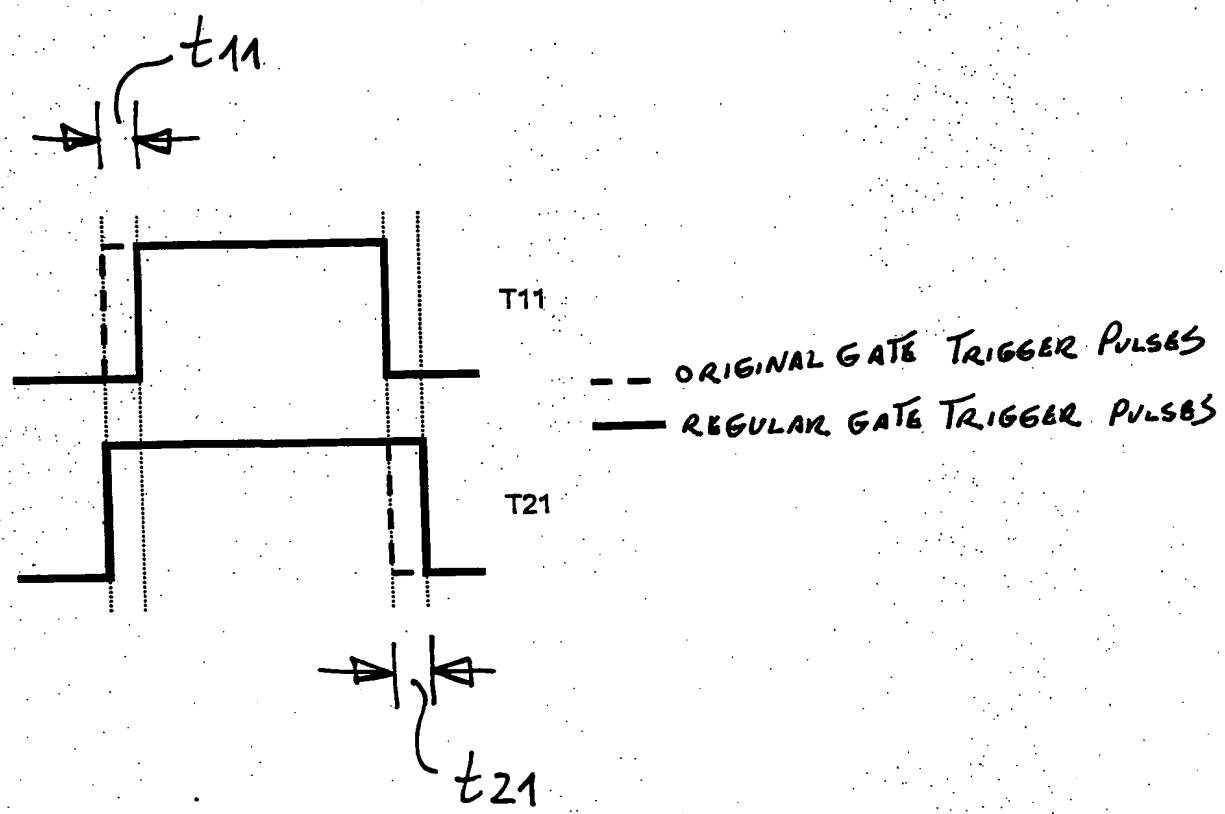


FIG. 2 GATE TRIGGER PULSES

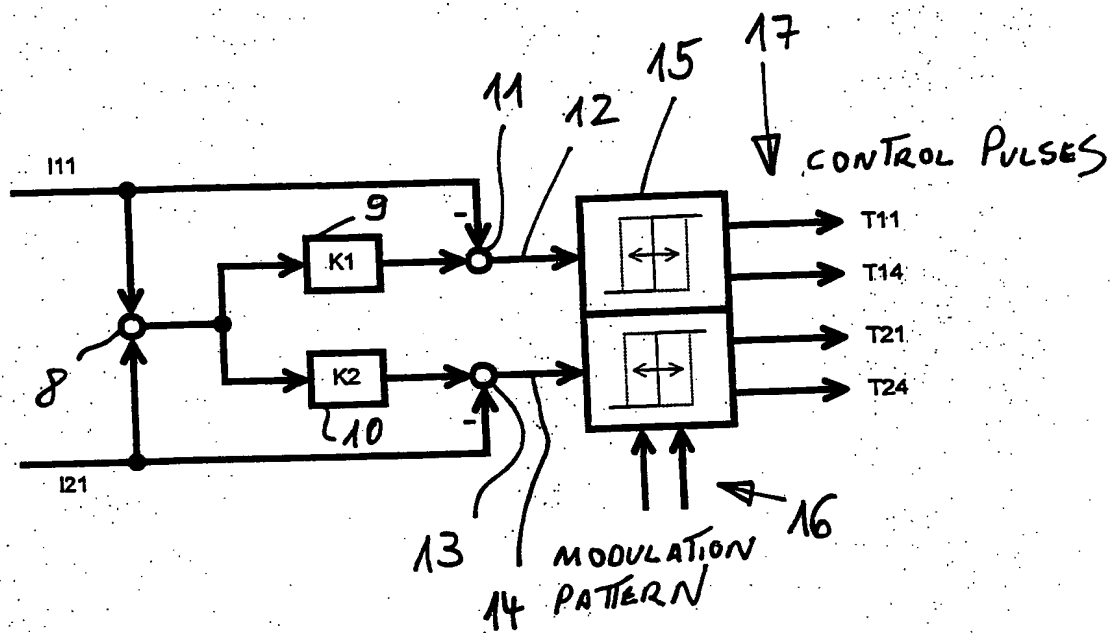


FIG. 3 BLOCK DIAGRAM OF THE REGULATION METHOD